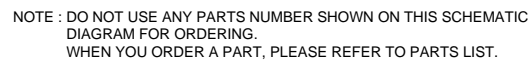


◀■: AUDIO MAIN SIGNAL PATH IN REC MODE
◀◻: AUDIO MAIN SIGNAL PATH IN PLAYBACK MODE

Figure 1: Block diagram of the system architecture. The diagram shows a central processing unit with various input and output ports. Inputs include S (SOPB/SE/VS), A MUTE, A L ch AUDIO IN, A TUN AUDIO IN, A NORA I.A IN, S PICT 1, S AVR (L), R.VIDEO IN, A FRONT V, and S GND. Outputs include B2, B3, B4, B5, B6, B7, B8, and B11 (-5.0V). The central unit contains several logic blocks: a multiplexer for audio inputs, a switch for video input, and a control logic block with a triangle symbol.



NV-FJ620A, NV-FJ620EA
VIDEO SECTION IN MAIN SCHEMATIC DIAGRAM